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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#7/a
3/18/3
Surles

Applicants: AKIMOTO et al.
Serial No.: 09/955,275
Filed: September 19, 2001
For: FIELD EFFECT TRANSISTOR AND IMAGE DISPLAY
APPARATUS USING THE SAME
Group: 2811
Examiner: G. Munson

AMENDMENT

Assistant Commissioner
for Patents
Washington, D.C. 20231

March 10, 2003

Sir:

In response to the Office Action dated October 8, 2002, the period of
response for which extension is requested by the attached Petition for Extension of
Time, please amend the above-identified application as follows:

In the Claims:

Please amend claims 1-16 as follows:

1. (Amended) An insulated gate field effect transistor having a gate
electrode overlapping a channel area between a first conductivity type source area
and a second conductivity type source area thereof, wherein:

said channel area provides a single channel between said first and
second conductivity type source areas and includes a non-overlapped area portion
free from being overlapped by the gate electrode in plan.

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02 FC:1202
03 FC:1201

108.00 OP
420.00 OP

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